

Nalanda College of Engineering Chandī, Nalanda Time Table (6th semester) 2026

W.E.F 1st July, 2026. Branch- Electrical & Electronics Engineering

Day	I	II		III	1.00 to 1.00	IV		V	VI
	10.00 to 11.00	11.00 to 12.00		12.00 to 1.00		02.00to 03:00	03:00 to 04:00		4.00 to 05:00
Monday	VLSI Design	MI		PE-II (ED)	L U N C H	VLSI Design Lab			Professional Skill Development VIK
Tuesday	Professional Skill Development VIK	MI		PE-I (EECA)		EDL Lab			Professional Skill Development VIK
Wednesday	DSP	DSP Lab				MI Lab			Remedial Class
Thursday	VLSI Design	PE-I (EECA)		DSP		MI Lab			Remedial Class
Friday	PE-I (EECA)	VLSI Design Lab				EDL Lab			VLSI Design
Saturday	PE-II (ED)	PE-II (ED)		MI	DSP		DSP Lab		

DSP **Mona Priya** **MP**
MI **Nikhil Nigam** **NN**
VLSI **Ashok Kumar Chaudh AKC**
PE-2(ED) **Dr Tripurari Das Gupta TDG**
PE-1(EECA) **Dr Sanjay Kumar** **DSK**
EDL Lab **Mona Priya** **MP**

HOD *[Signature]* **01.07.2026**
Department of EEE

[Signature]
Principal
Nalanda College of Engineering, Chandī